Agenda

What is a Virtual Machine?

The VM Language (Part 1)

- Stack arithmetic and logic
 - add, sub, neg, eq, gt, lt, and, or, not
- Virtual memory segments
 - push/pop commands

With slides from nand2tetris.org

Virtual machine

A **virtual machine** abstracts the hardware of a system using software

Virtual machines use a hardware-independent execution layer

Example: The Java virtual machine can run on any hardware

Example: VirtualBox allows you to simulate any operating system on your

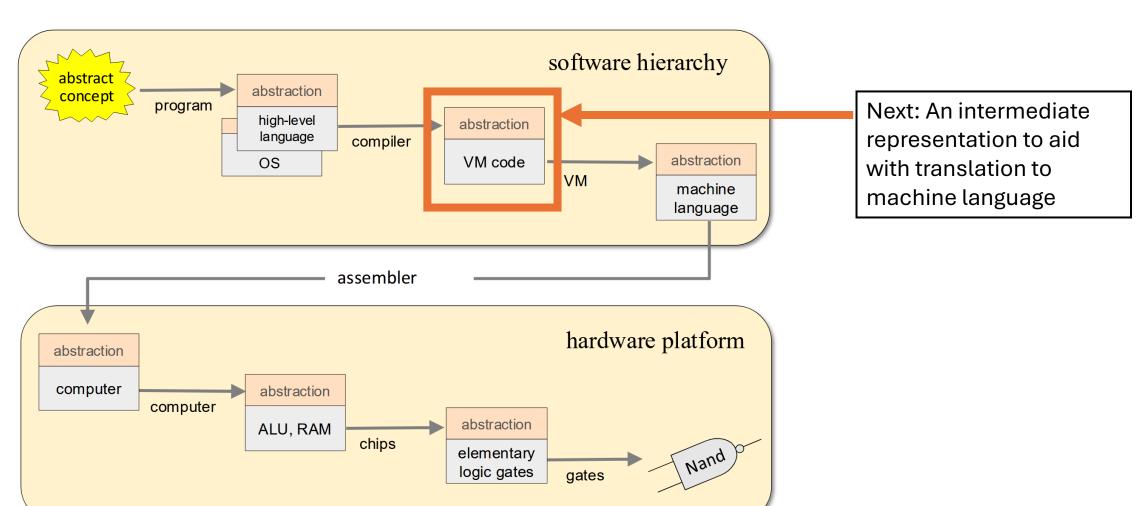
computer

Hack uses a virtual machine (VM) as an intermediary between high-level code and machine code

VM Code: Generated by the compiler

VM Translator: Translates VM code to machine code/assembly

The big picture: Hack



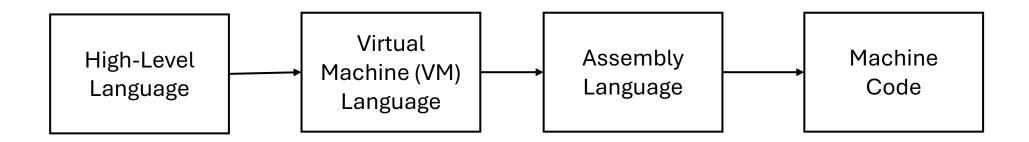
Recall: Compilation

How do we get from a high-level language to machine code?

Perform a series of translations that are increasingly low-level

Each level *abstracts* the details of the level below it

For example, assembly programs do not need to know the details of machine code



Writing a VM Translator for Hack

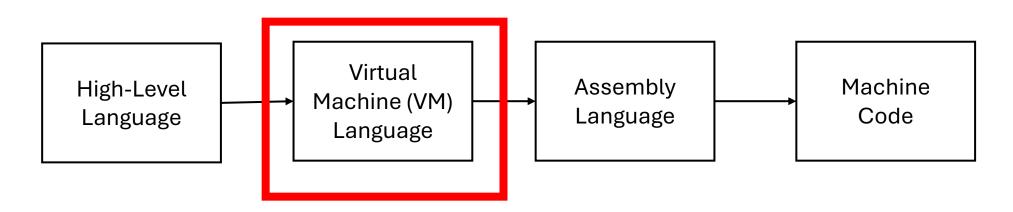
The VM translator converts high-level code to VM Code

VM code is entirely stack-based

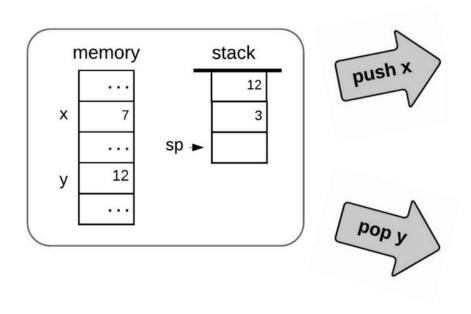
- Only uses push and pop
- Different stacks are used to store different types of variables

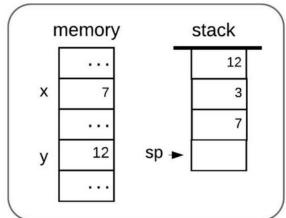
Two primary features:

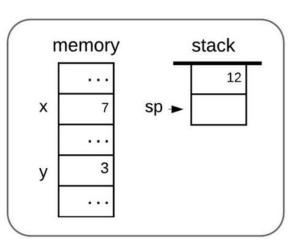
- Expressions (arithmetic, conditionals, etc)
- Function calls



Stacks







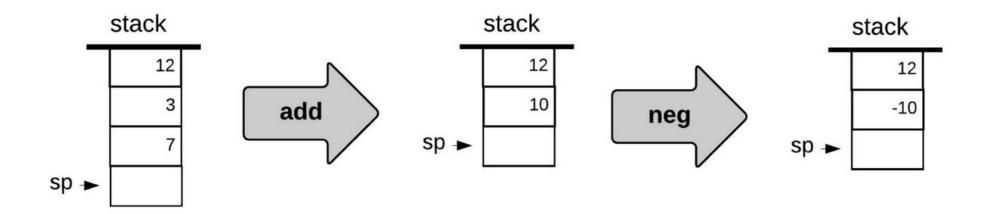
SP = Stack Pointer

Pop removes the topmost element

Push adds an element

Our stacks grow downwards

Idea: Implementing expressions with stacks



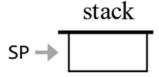
Applying a function f (that has n arguments)

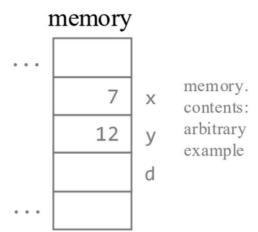
- pops *n* values (arguments) from the stack,
- Computes f on the values,
- Pushes the resulting value onto the stack.

Exercise: Stack arithmetic

VM pseudocode (example)

$$// d = (2-x) + (y+9)$$



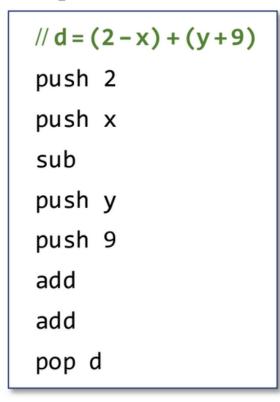


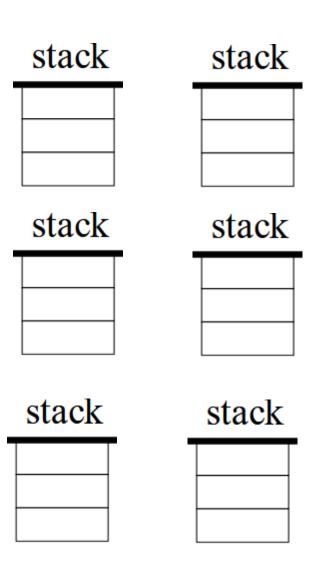
Convert the expression to VM code

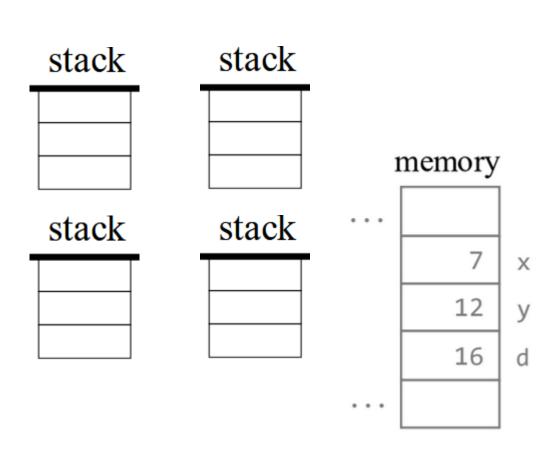
Exercise: Stack arithmetic

Visualize the VM Code

VM pseudocode

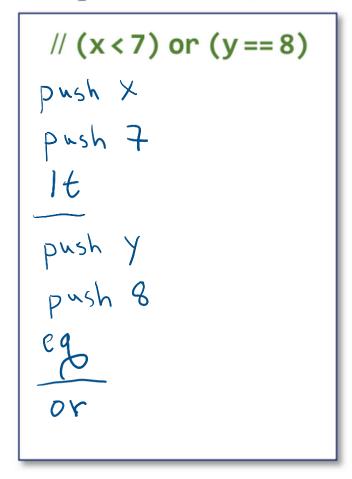


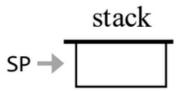




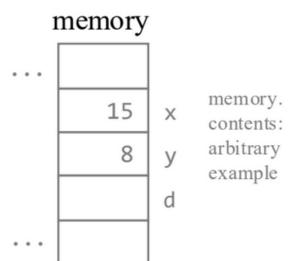
Exercise: Stack Logic

VM pseudocode (another example)





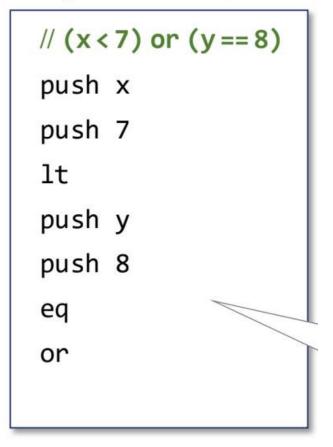
Write the VM Code

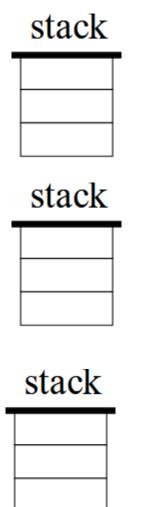


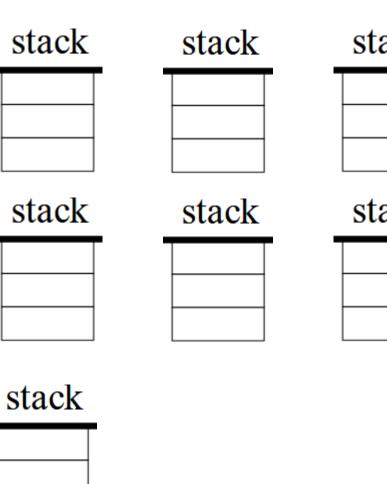
Visualize: Stack Logic

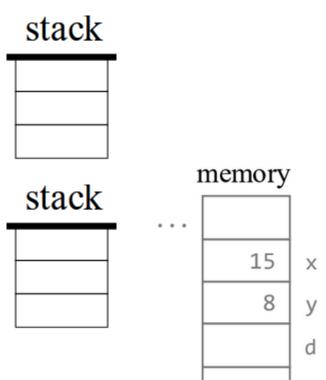
Visualize the VM Code

VM pseudocode (another



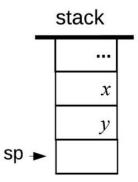






Stack functions: summary

| command | operation | returns |
|---------|---------------------|---------|
| add | x + y | integer |
| sub | x-y | integer |
| neg | - y | integer |
| eq | <i>x</i> == y | boolean |
| gt | x > y | boolean |
| 1t | <i>x</i> < <i>y</i> | boolean |
| and | x And y | boolean |
| or | x Or y | boolean |
| not | Not x | boolean |
| | ·- | |

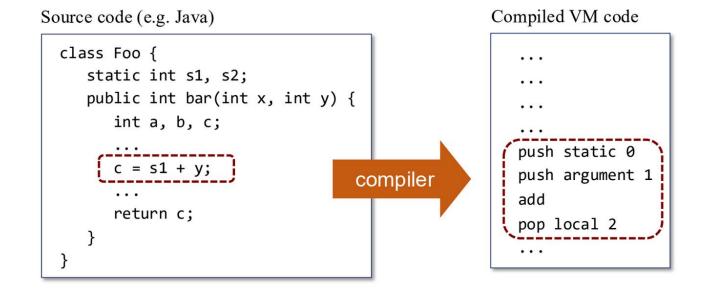


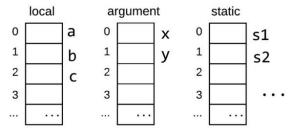
Each command pops as many operands as it needs from the stack, computes the specified operation, and pushes the result onto the stack.

Every high-level arithmetic or logical expressions can be translated to a sequence of VM commands, operating on a stack.

But how to handle the arguments to our functions?

Virtual memory segments





virtual memory segments

The compiler...

- 1. Represents variables by *virtual memory segments*, according to their *kinds*: local, argument, static, ...
- 2. Generates VM commands that operate on the stack and on the virtual memory segments.

Hack has 8 virtual memory segments:

- local
- argument
- static
- constant
- this
- that
- temp
- pointer

Syntax:

push / pop segment i

Exercise: Write VM Code that references virtual memory segments (VMS)

```
int global = 0;

void foo(int a, int b)
{
   global = a + 5;
}
```

High-level Code

Recall Syntax: push/pop segment i

How should we implement VM Code?

Where/how should our stacks be stored? Three Approaches:

Native: Extend the computer's hardware with modules that represent the stack, the stack pointer, and other VM constructs; Extend the computer's instruction set with primitive versions of the VM commands;

Emulation: Write a program in a high level language that represents the stack and the virtual memory segments as ADTs (abstract data type); Implement the VM commands as methods that operate on these ADTs;

Translation: Translate each VM command into machine language instructions that operate on a host RAM; Use an addressing contract that realizes the stack and the virtual address segments

How should we implement VM Code?

Where/how should our stacks be stored? Three Approaches:

Native: Extend the computer's hardware with modules that represent the stack, the stack pointer, and other VM constructs; Extend the computer's instruction set with primitive versions of the VM commands;

Emulation: Write a program in stack and the virtual memory s commands as methods the

The approach taken by:

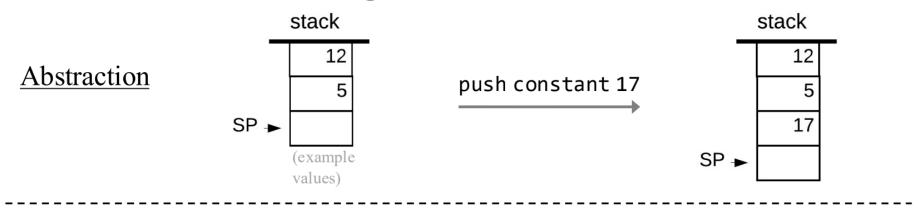
- Java, C#, Python, Ruby, Scala, ...
- Jack (designed in Nand to Tetris)

ts the

Translation: Translate each VM command into machine language instructions that operate on a host RAM; Use an addressing contract that realizes the stack and the virtual address segments

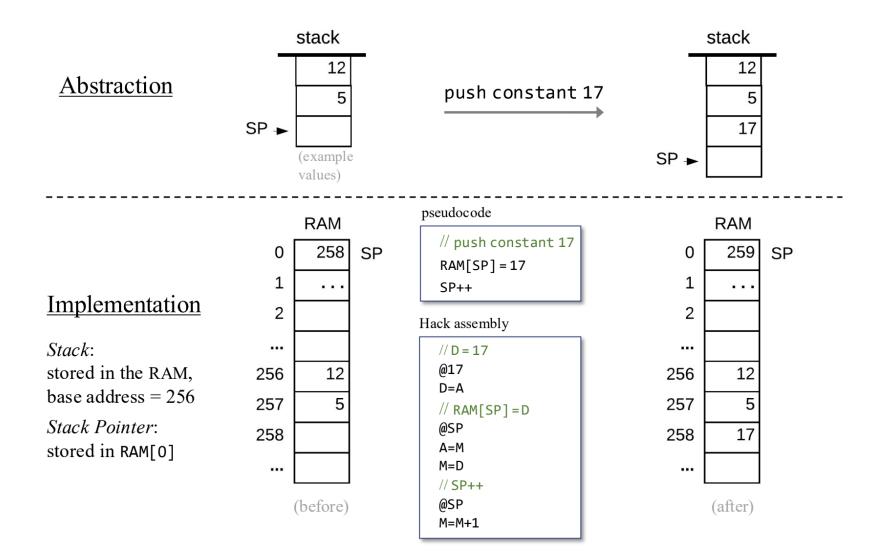
Abstraction: push/pos constant i

Constants (integers)

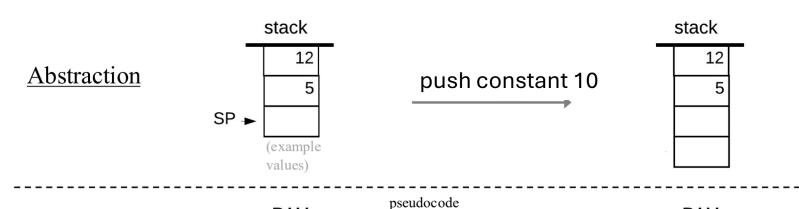


(before)

Implementing constants



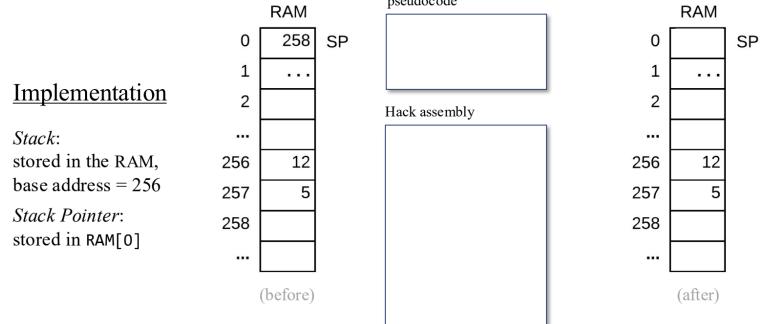
Exercise: Implementing constants



 $a = 10 \rightarrow push constant 10$

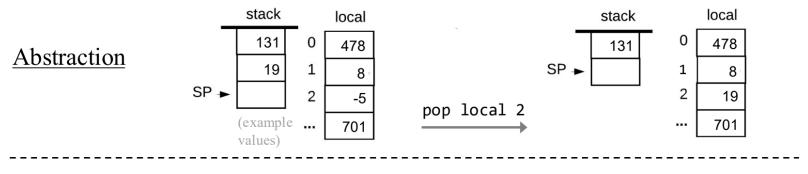
1: Convert to assembly pseudocode

2: Convert to assembly



Abstraction: push/pop local i

Local variables



pop local i pops the value at the working stack and stores it in local i

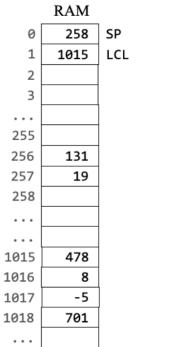
The local stack pointer (LCL is RAM[1]) stores the local stack segment location

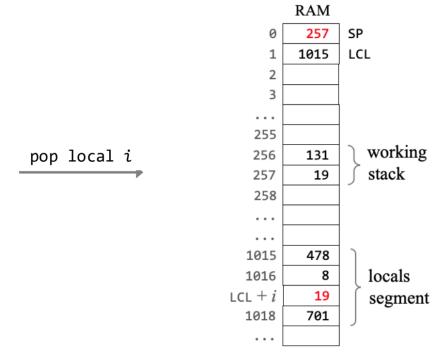
<u>Implementation</u>

locals segment: stored somewhere in the RAM;

LCL = base address

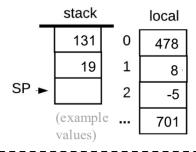
(1015 is an example)

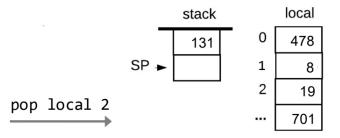




Exercise: Local variables

Abstraction



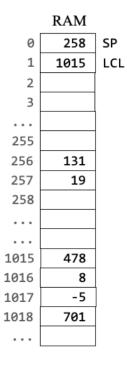


Implementation

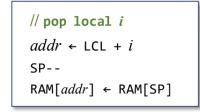
locals segment:
stored somewhere
in the RAM;
LCL = base address

ccc – base address

(1015 is an example)



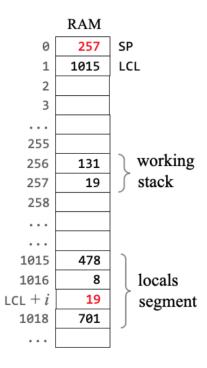
Pseudocode



pop local i

Hack assembly

You do it!



Summary: Local variables

Abstraction

VM code

 ${\tt pop local}\ i$

push local i



<u>Implementation</u>

Assembly pseudo code

```
// pop local i

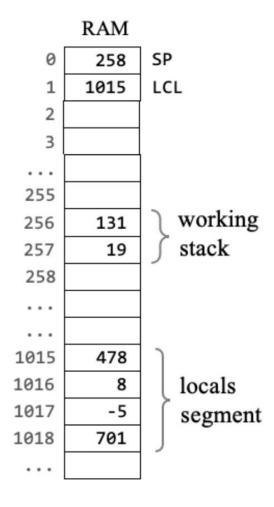
addr \leftarrow LCL + i

SP--

RAM[addr] ← RAM[SP]
```

```
// push local i
addr \leftarrow LCL + i
RAM[SP] \leftarrow RAM[addr]
SP++
```

Arguments, this, that, and local are all implemented the same way



Implementation of local, argument, this, that

Abstraction

VM code

pop segment i

push segment i

VM translator

where segment is local, argument, this, that and i is a non-negative integer

Implementation

Assembly pseudo code

```
// pop segment i

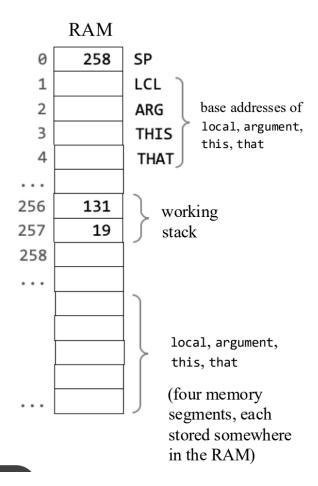
addr \leftarrow segmentPointer + i

SP--

RAM[addr] \leftarrow RAM[SP]
```

```
// push segment i
addr ← segmentPointer + i
RAM[SP] ← RAM[addr]
SP++
```

where *segmentPointer* is LCL, ARG, THIS, THAT



Implementation of local, argument, this, that

Abstraction: push/pop static i

Static variables

Location of class static variables

Standard mapping (contract)

The static segment is stored in a fixed RAM block, starting at address 16 and ending at address 255

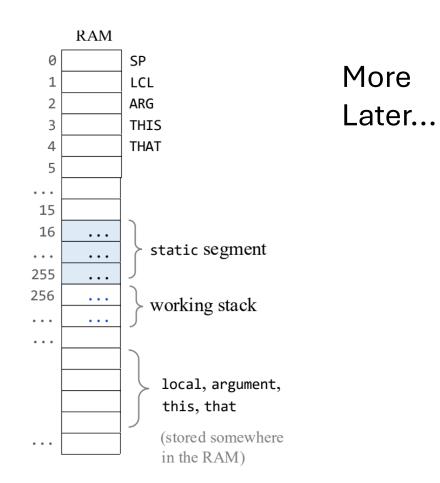
<u>To translate</u> push/pop static *i*

(when translating a VM file named XXX.vm)

Generate assembly code that realizes:

push/pop xxx.i

(Explanation: When this assembly code will be further translated to executable code, the Assembler will map these variables on RAM addresses 16, 17, 18, ..., exactly what we want).



Abstraction: push/pop temp i

Temporary variables

Sometimes we will need to define temporary variables that are not defined by the original code.

Stored in a fixed-size 8 variable stack, corresponding to *temp 0, temp 1,, temp 7*

The temporary stack segment is stored at RAM[5], Ram[6], ..., Ram[12]

Standard mapping (contract)

The temp segment is stored in a fixed RAM block, starting at address 5 and ending at address 12:

```
temp 0 is stored in RAM[5]
```

temp 1 is stored in RAM[6]

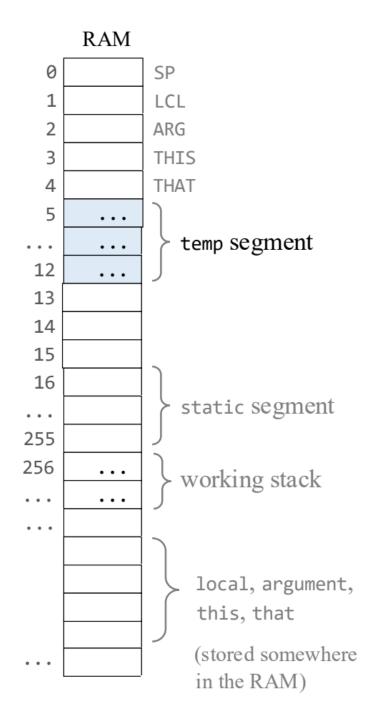
. . .

temp 7 is stored in RAM[12]

Implementing push/pop temp i

Generate assembly code that realizes:

push/pop RAM[5 + i]



Abstraction: push/pop pointer i

Pointer variables

Abstraction

pointer: A two-element segment, containing the base addresses of segments this and that

Implementation

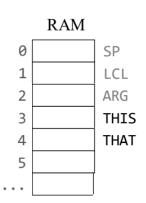
(a truly virtual segment, not stored anywhere)

To translate: push/pop pointer 0

generate assembly code that realizes push/pop THIS

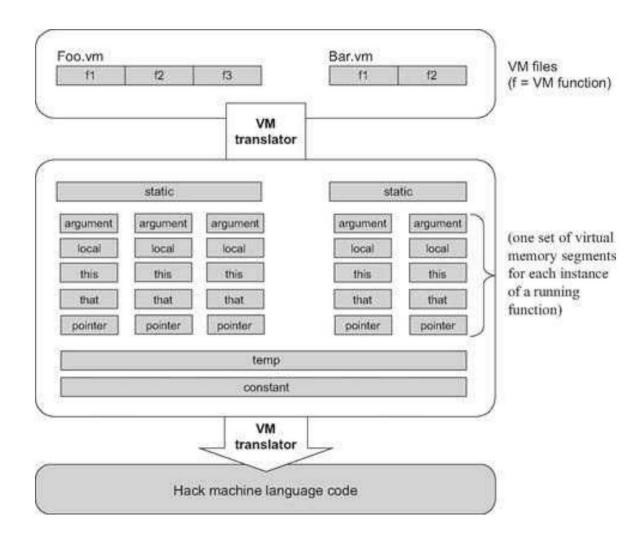
To translate: push/pop pointer 1

generate assembly code that realizes push/pop THAT

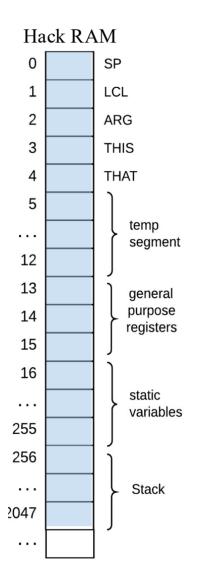


Used for objects and arrays (more later)...

Virtual memory segments for multiple files



Summary: Memory Access Commands



push segment index

Push the value of segment[index] onto the stack.

pop segment index

Pop the top stack value and store it in segment[index].

where segment ∈ {temp, static, this, that, local, constant, argument, pointer}

Exercise: Write VM code

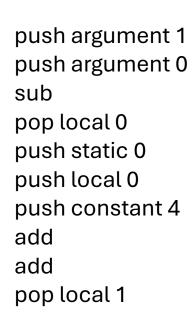
Suppose x1 = 10 and y1 = 2

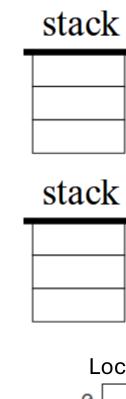
What virtual memory segments do we need?

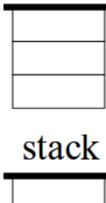
```
static int factor = 2;
int foo(int x1, int y1)
{
  int delta = y1 - x1;
  int tmp = factor + delta + 4;
  ....
}
```

Exercise: Visualize the VM code execution

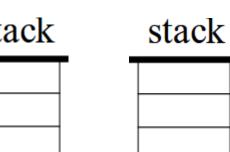
Suppose argument0 = 10 and argument1 = 2



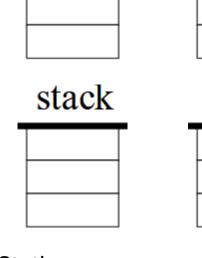




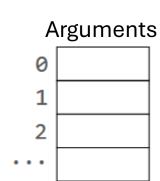
stack

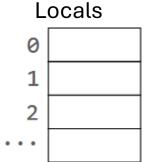


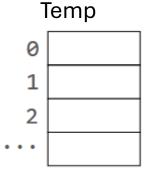
stack

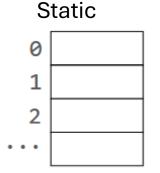


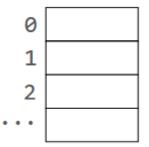
stack











stack

stack

Exercise: Convert to hack pseudocode

push argument 1 push argument 0 sub pop local 0

Exercise: Convert to hack assembly

```
// sub

SP--

D \leftarrow RAM[SP]

SP--

D \leftarrow RAM[SP] - D

RAM[SP] \leftarrow D

SP++
```